The aim of the rotary router is to improve the overall throughput of Network on Chips used in multi-core processors.

**Significance**

The Project looks to overcome the bottleneck that is experienced in multi-core processors. Using rotary routers as a Network on Chip to transfer information from memory to computer processors.

**Aims**

The aims of the project are:
- To design a rotary router using verilog
- To synthesize the rotary router onto a FPGA board
- To test the rotary router against the crossbar switch

**Outcomes**

The rotary router still needs to be finished. This involves completing the signals within the rotary router that control how it works.

**Components**

For the rotary router to be built it required a number of components to be designed in verilog.

- Multiplexer and Demultiplexer
- FIFO Buffer
- Counter
- SSRAM

**References**


